

11th November, 2022

NJG1804K64
3-bit Controlled SP3T Switch
S Parameter Simulation Data
0.1 GHz to 8.0 GHz

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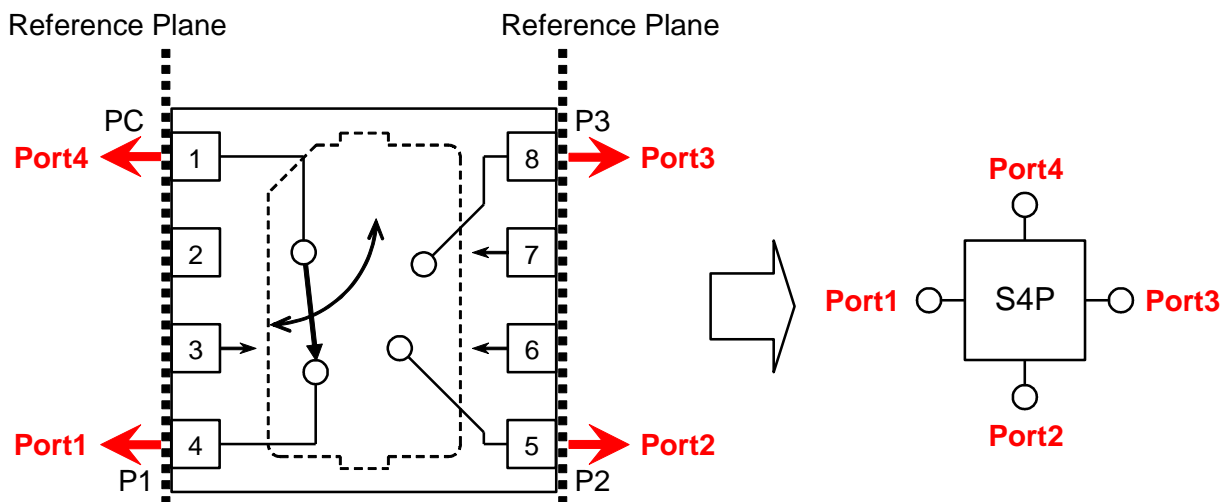
Nisshinbo Micro Devices Inc.



NJG1804K64 (3bit controlled SP3T switch)**■ Simulation condition**

- Frequency is from 0.1 GHz to 8.0 GHz, 10 MHz step
- $V_{CTL(H)} = 3.3\text{ V}$, $V_{CTL(L)} = 0\text{ V}$
- Unused RF ports are 50Ω terminated
- These S-parameters are included the package (DFN1515-8-GW) model
- The ports definition is shown as follow

Port1...P1 terminal	Port2...P2 terminal
Port3...P3 terminal	Port4...PC terminal

**■ S parameter data**

S4P file: touchstone format

File name: 1. PC-P1 ON mode..."NJG1804K64_PC-P1_ON_8GHz.s4p"
 2. PC-P2 ON mode..."NJG1804K64_PC-P2_ON_8GHz.s4p"
 3. PC-P3 ON mode..."NJG1804K64_PC-P3_ON_8GHz.s4p"

■ Simulation result

The S parameter simulation results are shown from Fig.1 to Fig.3 on the following pages.



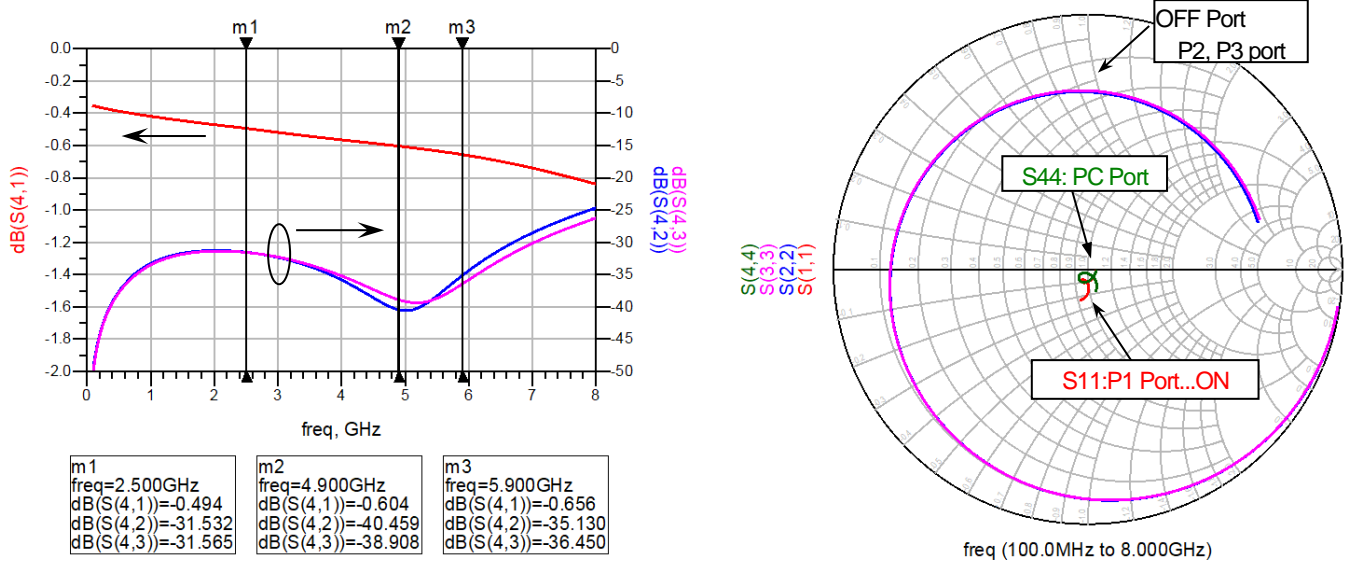


Fig.1 PC-P1 ON mode simulation ... "NJG1804K64_PC-P1_ON_8GHz.s4p"

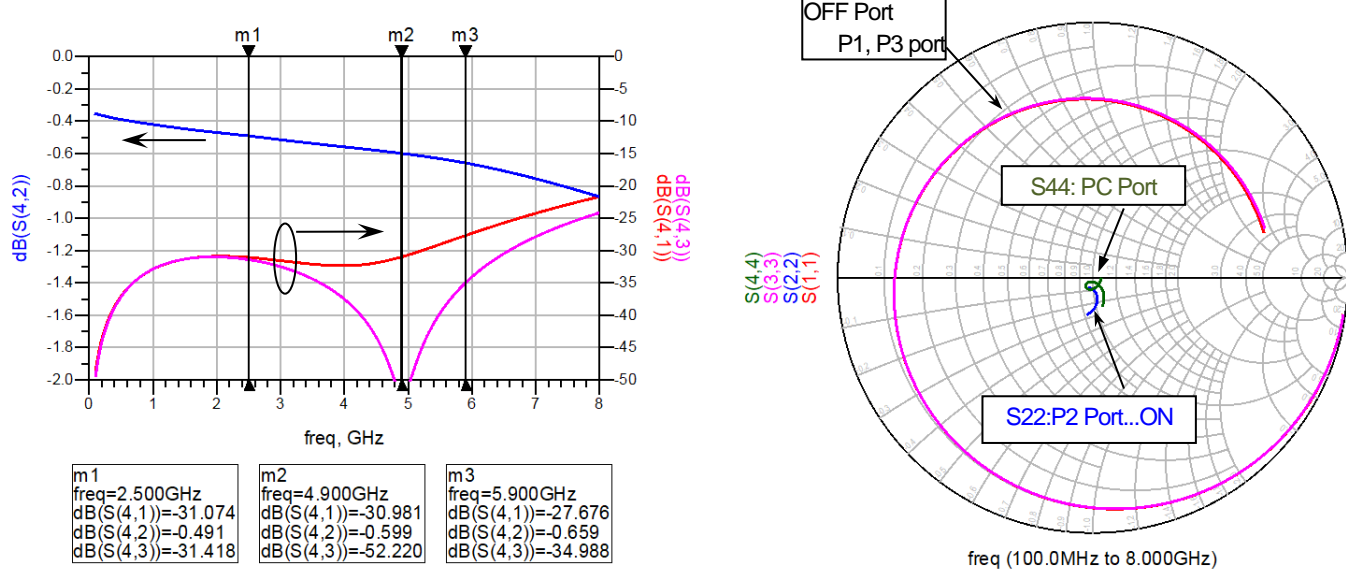


Fig.2 PC-P2 ON mode simulation ... "NJG1804K64_PC-P2_ON_8GHz.s4p"

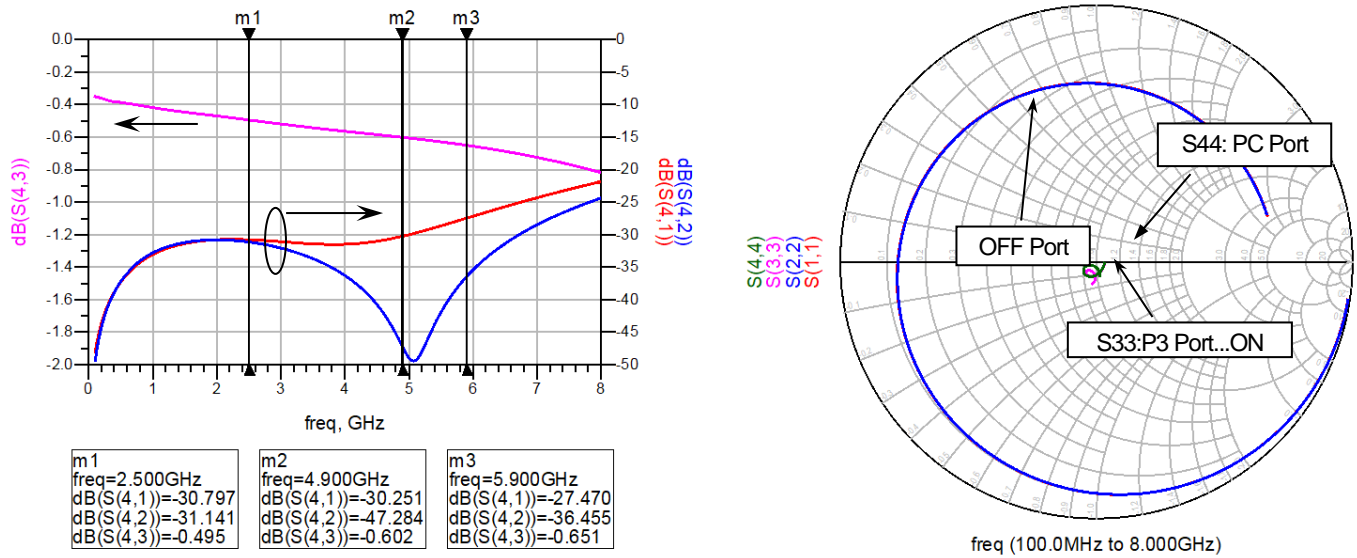


Fig.3 PC-P3 ON mode simulation ... "NJG1804K64_PC-P3_ON_8GHz.s4p"