

June 29, 2023

NJG1816K75
SPDT Switch
S Parameter Simulation Data
0.1 to 3GHz
Ver.1

Written by Daisuke Kurihara

Approved by Susumu Takagi

Nisshinbo Micro Devices Inc.

Electronic Devices Business Headquarters
Technology Development Division
RF Product Development Department
RFIC Design Section



■ **SIMULATION CONDITION**

- $f=0.1$ to 3GHz, Step=10MHz
- $V_{CTL}=0/1.8V$
- $T_a=25^{\circ}C$,
- $Z_L=Z_S=50\Omega$

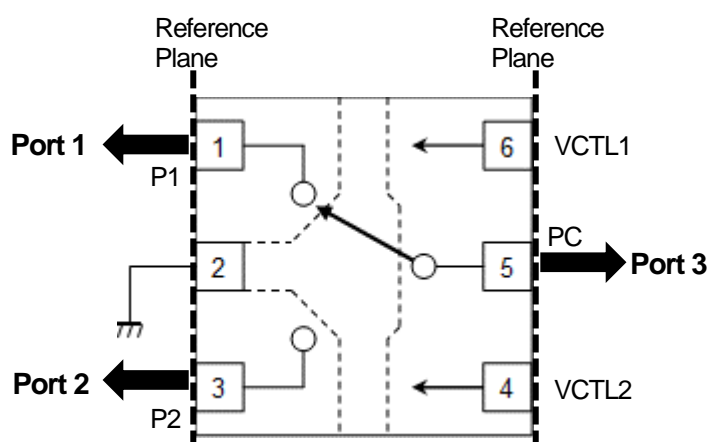
■ **DATA**

Touchstone (s3p)

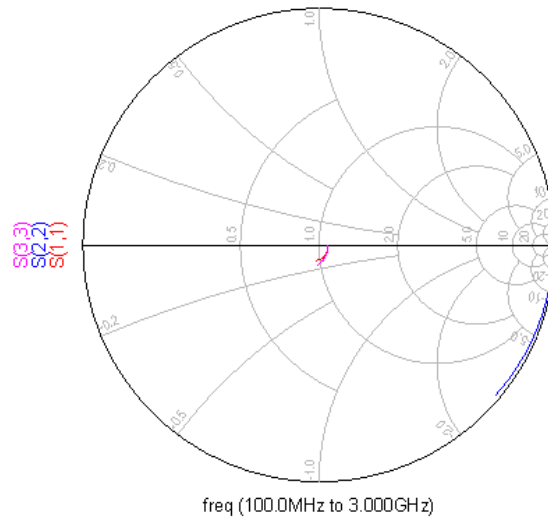
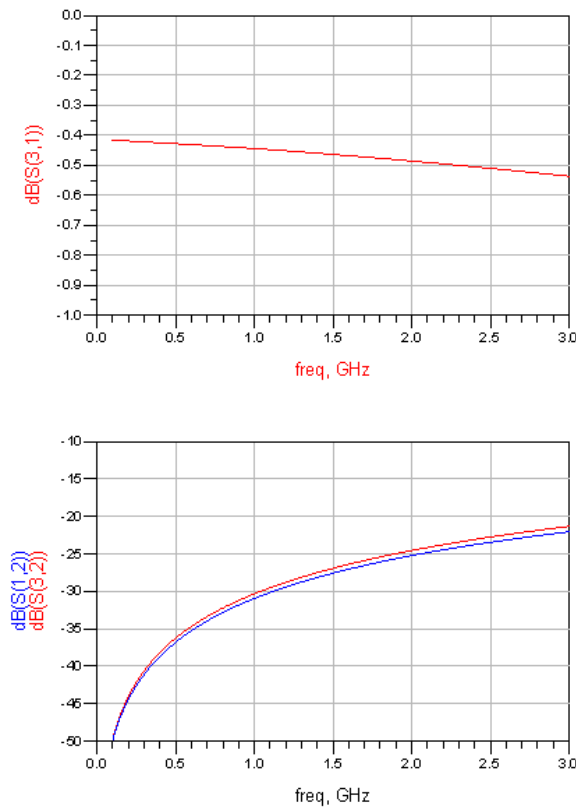
No.	Path	File name
1	PC-P1	NJG1816_P1on.s3p
2	PC-P2	NJG1816_P2on.s3p

■ **PORT DEFINITION**

Port No.	Terminal
1	P1 terminal
2	P2 terminal
3	PC terminal



File Name : NJG1816_P1on.s3p (PC-P1 ON mode)



File Name : NJG1816_P2on.s3p (PC-P2 ON mode)

