

December 12, 2022

**NJG1801BKGC-A**  
**SPDT Switch**  
**S Parameter Simulation Data**  
**0.1~8.5GHz**

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RF Product Development Department  
RFIC Design Section



■ **SIMULATION CONDITION**

- $f=0.1\sim 8.5\text{GHz}$ , Step=10MHz
- $V_{CTL}=0/3.0\text{V}$
- $T_a=25^\circ\text{C}$ ,
- $Z=Z_s=50\Omega$

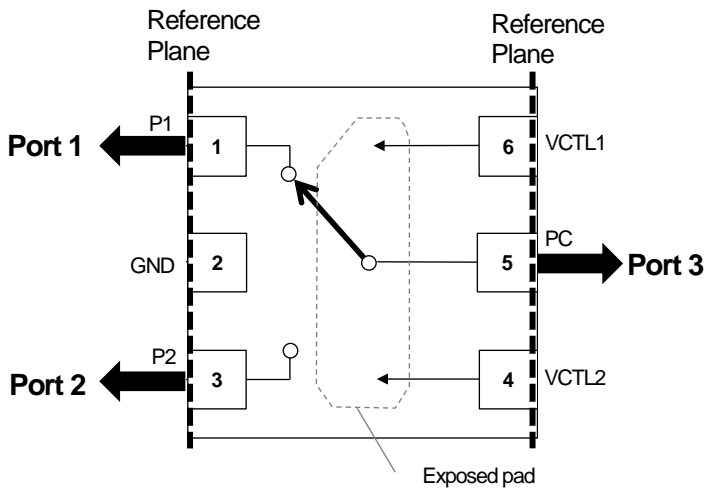
■ **DATA**

Touchstone (s3p)

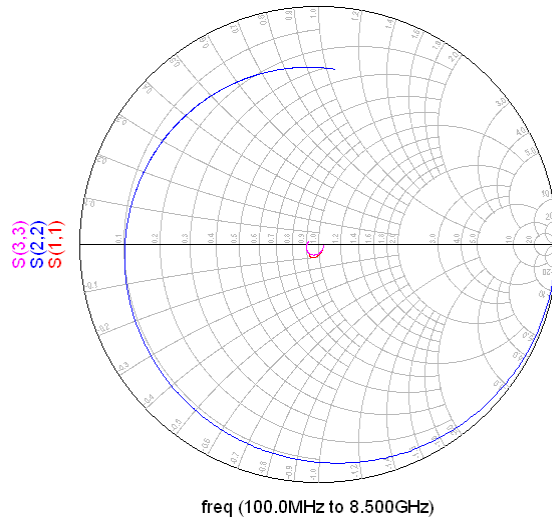
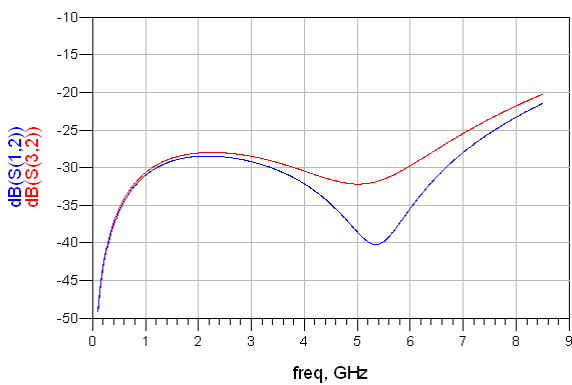
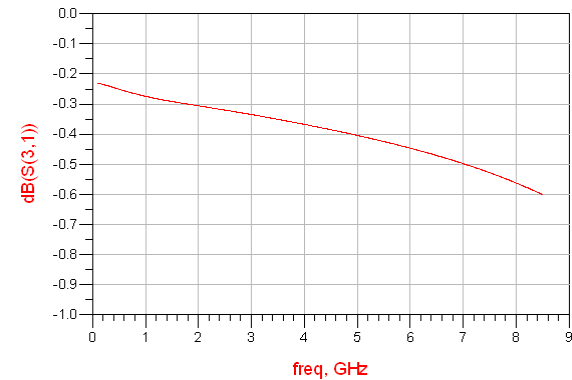
No.	Path	File name
1	PC-P1	NJG1801BKGC_P1on.s3p
2	PC-P2	NJG1801BKGC_P2on.s3p

■ **PORT DEFINITION**

Port No.	Terminal
1	P1 terminal
2	P2 terminal
3	PC terminal



File Name : NJG1801BKGC\_P1on.s3p (PC-P1 ON mode)



File Name : NJG1801BKGC\_P2on.s3p (PC-P2 ON mode)

